

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	IS&R	L1	0	("amortizingsame(clock adjcycles)").PN.	USPAT ; USOCR	2005/03/08 11:55	
2	IS&R	L2	0	("amortizingsame(clock adjcycles)").PN.	USPAT ; USOCR	2005/03/08 11:55	
3	BRS	L3	177	amortizing	USPAT	2005/03/08 11:55	
4	BRS	L4	0	amortizing same (clock adj cycles)	USPAT	2005/03/08 11:55	
5	BRS	L5	12	amortizing and (clock adj cycles)	USPAT	2005/03/08 11:55	
6	BRS	L6	186	simulation same (clock adj cycles)	USPAT	2005/03/08 11:56	
7	BRS	L7	27	(simulation same (clock adj cycles)) and (reduc\$ same (clock adj cycles))	USPAT	2005/03/08 11:57	
8	BRS	L8	79	(simulation same (clock adj cycles)) and (reduc\$ same (clock adj cycles))	USPAT	2005/03/08 11:57	
9	BRS	L9	27	(simulation same (clock adj cycles)) and (reduc\$ same (clock adj cycles)) and (flip-flops)	USPAT	2005/03/08 11:57	
10	BRS	L10	9	(simulation same (clock adj cycles)) and (reduc\$ same (clock adj cycles)) and ((flip-flops)same (elimate or reduc\$))	USPAT	2005/03/08 12:05	
11	BRS	L11	0	(simulation same (clock adj cycles)) and (reduc\$ same (clock adj cycles)) and ((flip-flops)same (elimate or reduc\$)) and (non-critical)	USPAT	2005/03/08 12:06	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
12	BRS	L12	0	(simulation same (clock adj cycles)) and (reduc\$ same (clock adj cycles)) and ((flip-flops) same (eliminate or reduc\$)) and (non adj critical)	USPAT	2005/03/08 12:07	
13	BRS	L13	2	(simulation and clock and cycles).ti.	USPAT	2005/03/08 12:11	
14	BRS	L14	2897	(simulation and (clock adj cycles))	USPAT	2005/03/08 12:11	
15	BRS	L15	159	(simulation and (clock adj cycles)) and (reduc\$ same (flip-flops))	USPAT	2005/03/08 12:13	
16	BRS	L16	140	(simulation and (clock adj cycles)) and (reduc\$ same (flip-flops)) and delay	USPAT	2005/03/08 12:13	
17	BRS	L17	49	(simulation and (clock adj cycles)) and (reduc\$ same (flip-flops)) and delay and (single adj clock)	USPAT	2005/03/08 12:14	
18	BRS	L18	0	(simulation and (clock adj cycles)) and (reduc\$ same (flip-flops)) and delay and (single adj clock) and unroller	USPAT	2005/03/08 12:14	
19	BRS	L19	49	(simulation and (clock adj cycles)) and (reduc\$ same (flip-flops)) and delay and (single adj clock) and time	USPAT	2005/03/08 12:28	
20	BRS	L20	24	(simulation and (clock adj cycles)) and (reduc\$ same (flip-flops)) and delay and (single adj clock) and time and (critical adj path)	USPAT	2005/03/08 12:28	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
21	BRS	L21	23	(simulation and (clock adj cycles)) and (reduc\$ same (flip-flops))and delay and (single adj clock)and time and (critical adj path)and latches	USPAT	2005/03/08 12:29	
22	BRS	L22	19	(simulation and (clock adj cycles)) and (reduc\$ same (flip-flops))and delay and (single adj clock)and time and (critical adj path)and (latches same (delete or eliminate))	USPAT	2005/03/08 12:32	
23	BRS	L23	0	(simulation and (clock adj cycles)) and (reduc\$ same (flip-flops))and delay and (single adj clock)and time and (critical adj path)and (latches near (delete or eliminate))	USPAT	2005/03/08 12:33	
24	BRS	L24	19	(simulation and (clock adj cycles)) and (reduc\$ same (flip-flops))and delay and (single adj clock)and time and (critical adj path)and (latches same(delete or eliminate))	USPAT	2005/03/08 12:34	
25	BRS	L25	0	(simulation and (clock adj cycles)) and (reduc\$ same (flip-flops))and delay and (single adj clock)and time and (critical adj path)and (eliminate adj latch)	USPAT	2005/03/08 12:34	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
26	BRS	L26	0	(simulation and (clock adj cycles)) and (reduc\$ same (flip-flops))and delay and (single adj clock)and time and (critical adj path)and (eliminate adj (flip-flops))	USPAT	2005/03/08 12:39	
27	BRS	L27	16	(simulation and (clock adj cycles)) and (reduc\$ same (flip-flops))and delay and ((single adj clock)same cycle)and time and (critical adj path)	USPAT	2005/03/08 12:51	
28	BRS	L28	0	(simulation and (clock adj cycles)) and (reduc\$ same (flip-flops))and delay and ((single adj clock)same cycle)and time and (critical adj path) and (shortes adj path)	USPAT	2005/03/08 12:52	
29	BRS	L29	0	(simulation and (clock adj cycles)) and (reduc\$ same (flip-flops))and delay and ((single adj clock)same cycle)and time and (critical adj path) and (shortest adj path)	USPAT	2005/03/08 12:52	
30	BRS	L30	1	(simulation and (clock adj cycles)) and (reduc\$ same (flip-flops))and delay and ((single adj clock)same cycle)and time and (shortest adj path)	USPAT	2005/03/08 13:13	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
31	BRS	L31	0	(simulation and (clock adj cycles)) and (reduc\$ same (flip-flops))and delay and ((single adj clock)same cycle)and time and (shortest adj path)and Internet	USPAT	2005/03/08 13:41	
32	BRS	L32	238	(simulation and (clock adj cycles)) and Internet	USPAT	2005/03/08 13:42	
33	BRS	L33	389	(simulation same Internet)	USPAT	2005/03/08 13:42	
34	BRS	L34	1	(simulation same Internet) and ((flip-flop) same reduction)	USPAT	2005/03/08 13:43	
35	BRS	L35	0	(simulation and Internet).ti.	USPAT	2005/03/08 13:43	
36	BRS	L36	389	(simulation same Internet)	USPAT	2005/03/08 13:44	
37	BRS	L37	2	(simulation same Internet) and ((clock adj cycles) same reduction)	USPAT	2005/03/08 13:47	
38	BRS	L38	70	(circuit adj design) same Internet	USPAT	2005/03/08 13:47	
39	BRS	L39	1	((circuit adj design) same Internet) and (design same (flip-flops))	USPAT	2005/03/08 13:50	
40	BRS	L40	0	((circuit adj design) same Internet) and (design same (flip-flops))and (shortest adj path)	USPAT	2005/03/08 13:51	
41	BRS	L41	1	((circuit adj design) same Internet) and (design same (flip-flops))and path	USPAT	2005/03/08 14:01	
42	BRS	L42	40	(shortest adj path) same (critical adj path)	USPAT	2005/03/08 14:02	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
43	BRS	L43	3	((shortest adj path) same (critical adj path)) and (clock adj cycles)	USPAT	2005/03/08 14:04	
44	BRS	L44	1	((shortest adj path) same (critical adj path)) and (clock adj cycles)and (circuit adj design)	USPAT	2005/03/08 14:17	
45	BRS	L45	0	((shortest adj path) same (critical adj path)) and (clock adj cycles)and (circuit adj design)and Internet	USPAT	2005/03/08 14:14	
46	BRS	L46	0	((shortest adj path) same (critical adj path)) and Internet	USPAT	2005/03/08 14:14	
47	BRS	L47	0	(Internter same (digital adj design))	USPAT	2005/03/08 14:15	
48	BRS	L48	2	(Internet same (digital adj design))	USPAT	2005/03/08 14:24	
49	BRS	L49	0	((shortest adj path) same (critical adj path)) and (clock adj cycles)and (circuit adj design) and reduction	USPAT	2005/03/08 14:17	
50	BRS	L50	1	((shortest adj path) same (critical adj path)) and (clock adj cycles)and (circuit adj design) and eliminate	USPAT	2005/03/08 14:17	
51	BRS	L51	0	(Internet near (digital adj design))	USPAT	2005/03/08 14:24	
52	BRS	L52	75	(Internet and (digital adj design))	USPAT	2005/03/08 14:24	
53	BRS	L53	18	(Internet and (digital adj design)) and (wireless and (digital adj design))	USPAT	2005/03/08 14:24	
54	BRS	L54	257	(remote and (digital adj design))	USPAT	2005/03/08 14:25	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
55	BRS	L55	22	(remote and (digital adj design)) and (remote same (Internet or wireless))	USPAT	2005/03/08 14:25	
56	BRS	L56	1	(collaborative adj engineering adj environment)	USPAT	2005/03/08 14:26	
57	BRS	L57	1	(collaborative adj engineering adj environment)and Internet	USPAT	2005/03/08 14:26	
58	BRS	L58	0	(collaborative adj engineering adj environment)and Internet and wireless	USPAT	2005/03/08 14:26	
59	BRS	L59	1	(collaborative adj engineering adj environment)and Internet and network	USPAT	2005/03/08 14:27	
60	BRS	L60	1	(collaborative adj engineering adj environment)and Internet and network and logic	USPAT	2005/03/08 15:07	
61	BRS	L61	0	(collaborative adj engineering adj environment)and Internet and network and (digital adj logic)	USPAT	2005/03/08 14:28	
62	BRS	L62	0	(collaborative adj engineering adj environment)and Internet and network and TTL	USPAT	2005/03/08 14:28	
63	BRS	L63	0	(collaborative adj engineering adj environment)and Internet and network and circuits	USPAT	2005/03/08 14:28	
64	BRS	L64	0	(over adj the adj Internet)	USPAT	2005/03/08 15:12	
65	BRS	L65	96	(simulation adj processors)	USPAT	2005/03/08 15:12	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
66	BRS	L66	28	(simulation adj processors)and (simulation same network)	USPAT	2005/03/08 15:13	
67	BRS	L67	0	(simulation adj processors)and (simulation same network)and (digital adj design)	USPAT	2005/03/08 15:13	
68	BRS	L68	4	(simulation adj processors)and (simulation same network)and (digital adj circuits)	USPAT	2005/03/08 15:13	